

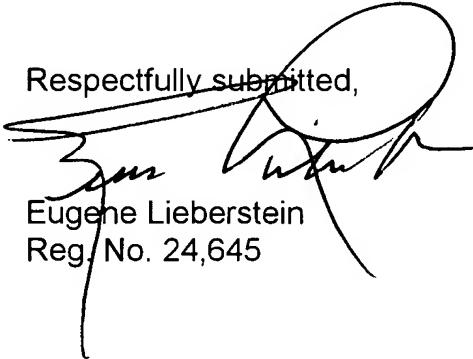
--Claim 9, step of forcing the first transistor to be turned OFF is performed by turning ON a switching element that is provided between the connection point between, the first and second transistors etc. --

In addition, form PTO-8820 (PTO 1449) filed with the application on February 27, 2004, and acknowledged by the Examiner by attaching a copy to the Office Action dated March 10, 2005, contains the Examiner's initials on sheet 1 but not on sheet 2. Accordingly, applicant would like the Examiner to initial sheet 2 of form PTO-1449, so that it is clear that document No. 2001-339986 dated 12/07/01 will be made of record on the front jacket of the issued patent.

Accordingly, applicant requests that a supplemental notice of allowance be issued for correcting the error on page 2, in the reasons for allowance and to acknowledge the prior art reference on sheet 2 of the PTO-8820 IDS dated February 27, 2004.

Should it not be possible for the Examiner to issue a Supplemental Notice of Allowance, applicant would like to be assured that the correction was made to the reasons for allowance and that the reference cited in form PTO-1449, attached to the Office Action dated March 10, 2005, is of record.

Respectfully submitted,

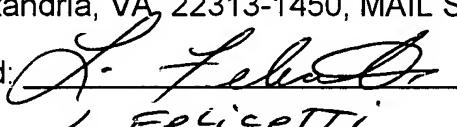

Eugene Lieberstein
Reg. No. 24,645

Customer # 01109

ANDERSON, KILL & OICK
1251 Avenue of the Americas
New York, New York 10020-1182
(212) 278-1307

MAILING CERTIFICATE

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as first class mail in an envelope addressed: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, MAIL STOP AF, on November 3, 2006.

Signed: 
L. Felicetti Dated: 11/30/, 2006